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PTO/SB/08a (07-05)

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Substitute for form 1449A/PTO		Complete if Known			
		Application Number	10/721,631		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Filing Date	11/25/2003		
		First Named Inventor	Blauvelt et al.		
		Art Unit	2874		
		Examiner Name	John D Lee		
Sheet	1	of	2	Attorney Docket Number	XPNT31NP

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No.	Document Number Number - Kind Code ³ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
ABZ		US- 2004-0052480 -- A1	03-18-2004	Benzoni et al	
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ABZ		US- 6,917,836 -- B2	07-12-2005	Blauvelt et al	
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		US-			
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Examiner Initials*	Cite No.	Foreign Patent Document Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶

Examiner Signature	John D. Lee	Date Considered	11 JAN. 2006
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Substitute for form 1449B/PTO			Complete If Known		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)			Application Number	10/721,631	
			Filing Date	11/25/2003	
			First Named Inventor	Blauvelt et al.	
			Art Unit	2874	
			Examiner Name	John D Lee	
Sheet	2	of	2	Attorney Docket Number	XPNT31NP

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
JDL		BAZYLENKO ET AL, Fabrication of Light-Turning Mirrors in Buried-Channel silica Waveguides for Monolithic and Hybrid Integration, Journal of Lightwave Technology, 01/1997, Page(s) 148-153, Volume 15, Number 1	
JDL		BOUADMA ET AL, 1.3-um GaInAsP/InP Buried-Ridge-Structure Laser and its Monolithic Integration with Photodetector Using RI Beam Etching, Journal of Lightwave Technology, 05/1994, Page(s) 742-748, Volume 12, Number 5	
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JDL		JONES ET AL, Hybrid integration onto silicon motherboards with planar silica waveguides, IEE Proc. Optoelectron., 10/1996, Page(s) 316-321, Volume 143, Number 5	

Examiner Signature	<i>John D. Lee</i>	Date Considered	11 JAN. 2006
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